Vishay Dale



Surface Mount Oscillator



The XOSM-532 series is an ultra miniature package clock oscillator with dimensions 5.0 x 3.2 x 1.3 mm. It is mainly used in portable PC and telecommunication devices and equipment.

FEATURES

- 5 x 3.2 x 1.3 Miniature Package
- Tri-state enable/disable
- HCMOS compatible
- Tape and Reel
- IR Re-flow
- 2.5 V input voltage
- 100 % Lead (Pb)-free and RoHS compliant

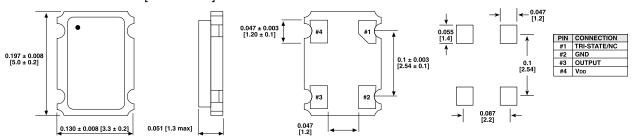


ROHS

PARAMETER	SYMBOL	CONDITION	XOSM-532		
Frequency Range	Fo		1.544 MHz ~ 100.000 MHz		
Frequency Stability*		All Condition*	± 25 ppm, ± 50 ppm, ± 100 ppm		
Operating Temperature	T _{OPR}		0 °C ~ 70 °C (- 40 °C ~ + 85 °C option)		
Storage Temperature Range	T _{STG}		- 55 °C ~ + 125 °C		
Power Supply Voltage	V_{DD}		2.5 V ± 10 %		
Aging (First Year)		25 °C ± 3 °C	± 5 ppm		
		1.544 MHz to 9.999 MHz	7 mA Max		
Supply Current	1	10.000 MHz to 34.999 MHz	8 mA Max		
Supply Current	I _{DD}	35.000 MHz to 49.999 MHz	20 mA Max		
		50.000 MHz to 100.000 MHz	30 mA Max		
Output Symmetry	Sym	At 1/2 V _{DD}	40/60 % (45/55 % Option)		
Rise Time	T _r	10 % V _{DD} ~ 90 % V _{DD}	6 ns Max		
Fall Time	T _f	90 % V _{DD} ~ 10 % V _{DD}	6 ns Max		
Output Voltage	V _{OH}		90 % V _{DD} Min		
Output voltage	V_{OL}		10 % V _{DD} Max		
Output Load HCMOS Lo	ad		30 pF Max (15 pF typ.)		
Start-up Time		Ts	10 ms Max		
Pin 1, tri-state function			Pin 1 = H or open output active at pin		
			Pin 1 = L high impedance at pin 3		

^{*} Include: 25 °C tolerance, operating temperature range, input voltage change, aging, load change, shock and vibration.

DIMENSIONS in inches [millimeters]



***note: A 0.01 µF bypass capacitor should be placed between VDD(Pin4) and GND(Pin2) to minimize power supply line noise

ORDER	ING IN	FORM/	ATION						
XOSM-532 MODEL	B FREQUENCY STABILITY AA = 0.0025 % (25 ppm) A = 0.005 % (50 ppm) B = 0.01 % (100 ppm) Standard		Blank = R = - 40 °	R OTR Blank = Standard R = - 40 °C to + 85 °C		E E/DISABLE ble to Tristate	50 M FREQUENCY/MI	e4 Hz JEDEC LEAD (Pb)-FREE STANDARD	
GLOBA	L PART	NUMI	BER						
Х	0	6	2	С	Т	E	A	N A	5 0 M
			FREQUENCY STABILITY	OTR	ENABLE/ DISABLE	PACKAGE CODE	OPTIONS	FREQUENCY	





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GLOBAL PART NUMBERING								
X O 5 2	С	Т	E	L	N A	4 0 M		
		_ ' _	<u> </u>	<u></u>				
MODEL NUMBER	FREQUENCY STABILITY	OPERATING TEMPERATURE (OTR)	ENABLE/ DISABLE	PACKAGE CODE	OPTIONS	FREQUENCY		
XO53 = XO-53	C = 0.01 %	T = 0 °C to + 70 °C	F = Pin 1 Open	TAPE AND	NA = No	4M = 4 MHz		
XO54 = XO-54	(100 ppm)	R=-40 °C to +85 °C	E = Disable to	REEL	Additional	40M = 40 MHz		
XO34 = XO-543	D = 0.005 %		Tristate	H = RF7	Options	100M = 100 MHz		
XO52 = XO-52	(50 ppm)				60 = 45/55	12M288 = 12.288 MHz		
XO32 = XO-523	E = 0.0025 %			BULK	Symmetry			
XO56 = XO-56	(25 ppm)			A = B04				
XOVC = XOVC-23				(XO63, XO62,		M is used as		
XO5M = XOSM-52				XO61)	Contact	decimal place		
XO63 = XOSM-533				C = D06	factory for	holder in frequency		
XO62 = XOSM-532				(XO57, XO37,	all other			
XO61 = XOSM-531				XO27, XO17)	options			
XO57 = XOSM-57				D = D07				
XO37 = XOSM-573				(XO53, XO54, XO34, XO56,				
XO27 = XOSM-572				XOVC, XO55,				
XO17 = XOSM-571				XO35)				
XO55 = XOSM-55				L = D08				
XO35 = XOSM-553				(XO52, XO32,				
				XO5M)				
				,	I			
Evennle, VOEOCTELNIA	10M							
Example: XO52CTELNA4	+UIVI							



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